



3-TERMINAL POSITIVE VOLTAGE REGULATOR

LM7805



TO-220 Leaded Plastic Package RoHS compliant

TO-220

GENERAL DESCRIPTION:

The Voltages Available allow these Regulators to be used in Logic Systems, Instrumentation, Hi-Fi Audio Circuits and other Solid State Electronic Equipment

APPLICATION:

Industrial Power Supplies, SMPS Post Regulation, HVAC Systems ,AC Inventors ,Test and Measurement Equipment,Brushed and Brush less DC Motor Drivers,Solar Energy String Inverters

ABSOLUTE MAXIMUM RATINGS (Ta = 25 °C Unless otherwise specified)

PARAMETER	SYMBOL	VALUE	UNIT
Input Voltage	V_{IN}	35	V
Continuous Total Dissipation at T _a =25°C free air Temperature	P_{D}	2.0	W
Continuous Total Dissipation at T _c =25°C case Temperature	P_{D}	15	W
Operating free-air, case, or Virtual Junction Temperature Range	T_{OPR}	0 to 150	°C
Storage Temperature Range	T_{stg}	-65 to +150	°C
Lead Temperature 1.6mm (1/16 inch) from Case for 10 seconds	T_L	260	°C







ELECTRICAL CHARACTERISTICS at (Ta = 25 °C Unless otherwise specified)

V_{IN}=10V, I_O=500mA

DADAMETED	CVMPOL	TEST CONDITION			VALUE		
PARAMETER	SYMBOL				TYP	MAX	UNIT
			T _j =25°C	4.9		5.10	V
Output Voltage	V _o	I_0 =5mA ~ $1_A V_1$ =7V ~ 20V, P<15W	T _j =0 ~ 125°C	4.8		5.20	V
Line Regulation	R _{EGV}	VI=7.0 ~ 25V	T _i =25°C			100	mV
	· 'EGV	VI=8.0 ~ 12V	., 20 0			50	mV
Ripple Rejection	R_R	V_1 =8.0 ~ 18V, f=120Hz	T _j =0 ~ 125°C	62			dB
Load Bagulatian	_D	I _O =5mA ~ 1.5A	T = 250C			100	mV
Load Regulation	R_{EGL}	I _O =250mA ~ 750mA	T _j =25°C			50	mV
Output Resistance	R _o	f=1KHz	T _j =0 ~ 125°C		0.017		Ω
Output Voltage Drift	$\Delta V_{O}/\Delta T$	I _O =5mA	T _j =0 ~ 125°C		-1.1		mV/°C
Output Noise Voltage	V_{NO}	f=10Hz ~ 100KHz	T _j =25°C		40		μV
Dropout Voltage	V_d	I _O =1A	T _j =25°C		2.0		V
Quiescent Current	IQ		T _j =25°C			8.0	mA
Quigagent Current Change	A.I.	V₁=7.0 ~ 25V	T=0 -: 1259C	-		1.3	mA
Quiescent Current Change	ΔI_{Q}	I _O =5mA ~ 1A	T _j =0 ~ 125°C			0.5	mA
Short Circuit Output Current	I _{sc}		T _j =25°C	-	750		mA
Peak Output Current	I _{PK}		T _j =25°C		2.2		Α



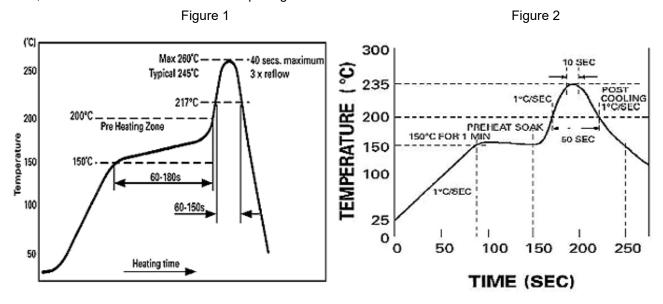


Recommended Reflow Solder Profiles

The recommended reflow solder profiles for Pb and Pb-free devices are shown below.

Figure 1 shows the recommended solder profile for devices that have Pb-free terminal plating, and where a Pb-free solder is used.

Figure 2 shows the recommended solder profile for devices with Pb-free terminal plating used with leaded solder, or for devices with leaded terminal plating used with a leaded solder.



Reflow profiles in tabular form

Profile Feature	Sn-Pb System	Pb-Free System
Average Ramp-Up Rate	~3°C/second	~3°C/second
Preheat - Temperature Range - Time	150-170°C 60-180 seconds	150-200°C 60-180 seconds
Time maintained above: – Temperature – Tim	200°C 30-50 seconds	217°C 60-150 seconds
Peak Temperature	235°C	260°C max.
Time within +0 -5°C of actual Peak	10 seconds	40 seconds
Ramp-Down Rate	3°C/second max.	6°C/second max





An IATF 16949, ISO9001 and ISO 14001/ISO 45001 Certified Company

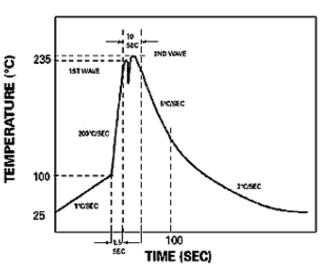
Recommended Wave Solder Profiles

The Recommended solder Profile For Devices with Pb-free terminal plating where a Pb-free solder is used

TO SEC 200°C/SEC 200°C/SEC

TIME (SEC)

The Recommended solder Profile For Devices with Pb-free terminal plating used with leaded solder, or for devices with leaded terminal plating used with leaded solder



Wave Profiles in Tabular Form

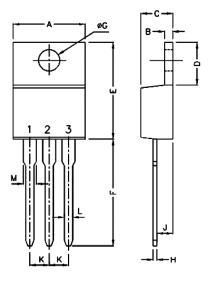
Profile Feature	Sn-Pb System	Pb-free System
Average Ramp-Up Rate	~200°C/second	~200°C/second
Heating rate during preheat	Typical 1-2, Max 4°C/sec	Typical 1-2, Max 4°C/Sec
Final preheat Temperature	Within 125°C of Solder Temp	Vithin 125°C of Solder Tem
Peak Temperature	235°C	260°C max.
Time within +0 -5°C of actual Peak	10 seconds	10 seconds
Ramp-Down Rate	5°C/second max.	5°C/second max.





PACKAGE DETAILS

TO-220 Leaded Plastic Package



DIM	MIN	TYP	MAX
Α	-		10.7
В	-	-	1.4
С	-	-	4.8
D	-	-	6.9
Е	-		16.5
F	12.5		
G	-	3.81	
Н	-		0.4
J	-	2.67	
K		2.51	
L			1.2
М		1.27	

All Dimensions are in mm

PIN CONFIGURATION

- 1. Input
- 2. Ground
- 3. Output





Recommended Product Storage Environment for Discrete Semiconductor Devices

This storage environment assumes that the Diodes and transistors are packed properly inside the original packing supplied by CDIL.

- · Temperature 5 °C to 30 °C
- · Humidity between 40 to 70 %RH
- · Air should be clean.
- · Avoid harmful gas or dust.
- · Avoid outdoor exposure or storage in areas subject to rain or water spraying .
- · Avoid storage in areas subject to corrosive gas or dust. Product shall not be stored in areas exposed to direct sunlight.
- · Avoid rapid change of temperature.
- · Avoid condensation.
- · Mechanical stress such as vibration and impact shall be avoided.
- · The product shall not be placed directly on the floor.
- $\cdot\,$ The product shall be stored on a plane area. They should not be turned upside down.

They should not be placed against the wall.

Shelf Life of CDIL Products

The shelf life of products is the period from product manufacture to shipment to customers. The product can be unconditionally shipped within this period. The period is defined as 2 years.

If products are stored longer than the shelf life of 2 years the products shall be subjected to quality check as per CDIL quality procedure.

The products are further warranted for another one year after the date of shipment subject to the above conditions in CDIL original packing.

Floor Life of CDIL Products and MSL Level

When the products are opened from the original packing, the floor life will start.

For this, the following JEDEC table may be referred:

JEDEC MSL Level			
Level	Time	Condition	
1	Unlimited	≤30 °C / 85% RH	
2	1 Year	≤30 °C / 60% RH	
2a	4 Weeks	≤30 °C / 60% RH	
3	168 Hours	≤30 °C / 60% RH	
4	72 Hours	≤30 °C / 60% RH	
5	48 Hours	≤30 °C / 60% RH	
5a	24 Hours	≤30 °C / 60% RH	
6	Time on Label(TOL)	≤30 °C / 60% RH	





Customer Notes

Component Disposal Instructions

- 1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving /support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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